

ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

U.S. UTILITY Patent Application

Q.I.P.E. SCANNED <i>240</i> Q.A. <i>27</i>	PATENT DATE
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APPLICATION NO. 09/749725	CONT/PRIOR	CLASS 712	SUBCLASS 215	ART UNIT 2183	EXAMINER Chen, Eddie
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Method and apparatus for distributed processor dispersal logic

PTO-204
12/89

ISSUING CLASSIFICATION

[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner)		NOTICE OF ALLOWANCE MAILED	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____ _____ _____	_____ (Primary Examiner)		ISSUE FEE	
	_____ (Date)		Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner)		ISSUE BATCH NUMBER	
		_____ (Date)		

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